### CSE TERMINAL MASTERS PLAN OF STUDY in VLSI

Name: ______________________________________  UMID: __________________________  Advisor (signature required) ________________________

Date: ____________________________

MSE Degree or MS Degree (circle one) ____________________________

(Date)

(What is your undergrad degree field? (engineering or non-engineering) ____________________________

Degree Term: ____________________________

<table>
<thead>
<tr>
<th>Term</th>
<th>Course</th>
<th>Grade</th>
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<tr>
<th>Credit Hours</th>
<th>Technical Electives:</th>
<th>CSE Courses</th>
<th>500 level or above</th>
<th>Cognates:</th>
<th>427 and 627 (BOTH)</th>
<th>VLSI Kernel Requirements</th>
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**NOTES:**

1) Cognates are courses outside the CSE Division (Courses cross-listed with CSE may not be used, and all cognates must be approved by advisor)

2) A maximum of six credit hours of individual study, research, and seminars

3) You must meet all Rackham and Program requirements (see brochures for details)

4) It is expected that most entering students will have already completed courses equivalent to (482 or 483) and 492 and (470 or 478)

5) Seminar, directed study credits (except 3 hrs. of EECS 599) do not count toward the 500 level course requirement

6) It is the student’s responsibility to see that all requirements are met.

7) You must choose 2 of the 4 areas in addition to the VLSI Kernel

8) one of the 500 level must be from the approved list/see brochure

9) If you already have a masters degree from another institution that has been deemed relevant by CSE you are not eligible for a masters degree from this program.

**Total Hours:**

(fill in for each column)

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No grades below B-

Approved MPS

Other, Masters thesis, TC

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GPA | CTP

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08/16